Arm® Cortex®-M

32-bit Microcontroller

NuMicro® Family
KM1M7CF0 CMSIS BSP
Revision History

The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.

Nuvoton is providing this document only for reference purposes of NuMicro microcontroller based system design. Nuvoton assumes no responsibility for errors or omissions.

All data and specifications are subject to change without notice.

For additional information or questions, please contact: Nuvoton Technology Corporation.

www.nuvoton.com

Revision 1.00.000a (Released 2022-04-25)

1. Preliminary release version.

Revision 1.00.000b (Released 2022-05-31)

1. Preliminary release version.

Revision 1.00.000c (Released 2022-06-30)

1. Preliminary release version.
   Library release only. Sample code is not included.

Revision 1.00.000d (Released 2022-08-03)

1. Preliminary release version.
   - Library update.
   - Add Sample code. (SYS, CLK, GPIO, WDT, TIMER, LVD, CKM, GPWM, MFA, DAC, CMP)

Revision 1.00.000e (Released 2022-08-10)

1. Preliminary release version.
   - There are no changes to the documentation and library.
   - Add Sample code. (ADC)

Revision 1.00.000f (Released 2022-09-09)

1. Preliminary release version.
   - Library/StdDriver update.
   - Add Library/XMODEM
   - Add Sample Code. (UART, SPI, I2C, SMBus, CRC, FMC)

Revision 1.00.000g (Released 2022-10-14)

1. Preliminary release version.
   - Add Sample code. (SYS, CLK, ADC, DAC, I2C, SMBus, DMA, CAN, RTC, LIN)
   - Fixed Library. (CLK, TIMER, I2C, SMBus)
   - Fixed printf output messages. (Sample Code)
   - Fixed to insert a full interrupt disable setting before the interrupt setting of each function. (Sample Code)

Revision 1.00.000h (Released 2023-2-22)

1. Preliminary release version.
   ・Fixed Library. (CKM, CLK, CMP, DMA, FMC, GPIO, GPWM, I2C, MFA, TIMER, I2C, SMBus, ECC)
   ・Fixed Sample Code.

Revision 1.00.000 (Released 2023-3-31)

Official release version.
1. Updated Library. (ADC, FMC, Device)
2. Added ISP Sample Code.
3. Updated Sample Code. (CKM, FMC, LVD)

Revision 1.00.001a (Released 2023-7-28)

Preliminary release version.
1. Updated Library. (I2C, SMBUS, CLK)

Revision 1.00.002 (Released 2024-1-19)

Official release version.
1. Updated Library. (ADC)
2. Updated Sample Code. (ADC, ISP)

Revision 1.00.003 (Released 2024-6-28)

Official release version.
1. Updated Library. Added a apache license comment. (Device,RTC)
2. Updated ISP Sample Code.
3. Updated Sample Code. Added a apache license comment. (FMC,ISP)

Important Notice

Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".

Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.

All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.

Please note that all data and specifications are subject to change without notice.
All the trademarks of products and companies mentioned in this datasheet belong to their respective owners.